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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/053.130
		Filing Date	11/13/2001
		First Named Inventor	Orly Yadid-Pecht
		Group Art Unit	2621
		Examiner Name	Couso, Jose L.
		Attorney Docket Number	56156628-2
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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
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Dms	1	KEMENY, S., et al; "Multiresolution Image Sensor"; Systems for Video Technology; Vol. 7, No. 4; August 1997; pp. 575-582	
	2	STROBACH, P.; "Quadtree-Structured Interframe Coding of HDTV Sequences"; SPIE Vol. 1001 Visual Communications and Image Processing '88; November, 1988; pp. 812-819	
	3	DICKINSON, A., et al; "A 256x256 CMOS Active Pixel Image Sensor with Motion Detection"; 1995 IEEE International Solid-State Circuits Conference; February 16, 1995; pp. 226-227	
	4	XIA, S., et al; "An All CMOS Foveal Image Sensor Chip"; IEEE International Asic Conference and Exhibit; 1998; pp. 409-413	
	5	AIZAWA, K., et al; "A Novel Image Sensor for Video Compression"; Proceedings Icip-94: Austin Convention Center Austin, Texas; November 13, 1994; pp. 591-595	
	6	PANICACCI, R., et al; "Programmable multiresolution CMOS active pixel sensor"; SPIE Vol. 2654 Proceedings, Intl. Conf. on Image Processing; Vol. II of III; October 26-29, 1997; pp. 72-79	
	7	PARDO, F., et al; "Space-Variant Nonorthogonal Structure CMOS Image Sensor Design"; IEEE Journal of Solid-State Circuits; Vol. 33, No. 6; June 1998; pp. 842-849	
	8	AIZAWA, K., et al; "Implementations of On Sensor Image Compression and Comparisons Between..."; Proceedings, 1st. Intl. Conf. on Image Processing; October 26-29, 1997; pp. 258-261	
Dms	9	KAWAHITO, S., et al; "A CMOS Image Sensor with Analog Two-Dimensional DCT-Based Compression..."; IEEE Journal of Solid-State Circuits; Vol. 32, No. 12; Dec. 1997; pp. 2030-2041	

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